



EFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: Phat X. Cao

In Re PATENT APPLICATION Of:

Applicant(s): Nobuhisa KUMAMOTO et al.)
Serial No: 10/767,439)
Filed: January 30, 2004)
For: SEMICONDUCTOR CHIP AND)
PRODUCTION PROCESS)
THEREFOR)
Docket No.: AI 318D1)

**RESPONSE TO
RESTRICTION REQUIREMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Restriction Requirement dated July 5, 2005, applicants elect Invention I, i.e., the subject matter of claims 11 and 12, for further examination. The election is without traverse.

Examination of the application on the merits is respectfully requested.

Respectfully submitted,

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SMR:pjl

August 3, 2005
Date

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